## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1042	gate and (sidewall adj spacer) and (ild or (interlayer adj dielectric))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 15:15
S3	0	substrate and (gates adj connected) adj "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:34
S4	4486	substrate and (gates adj connected) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:35
S5	2783	substrate and (gate adj electrodes adj connected) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:53
S6	148	substrate and (gates adj connected adj together) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:36
S7	0	substrate and (gates adj connected adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:37
S8	67	substrate and (gates adj connected adj series) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 12:37
S9	5	substrate and (gates adj electrically adj coupled adj together) and "257"/\$. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 13:35

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S10	538	substrate and (gates adj electrically adj connected) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 13:00
S11	672	substrate and (gate adj electrodes adj electrically adj connected) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:30
S12	0	substrate and (gates adj electrically adj coupled adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:24
S13	0	substrate and (gates near electrically adj coupled adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 13:36
S14	0	substrate and (gates with electrically adj coupled adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 13:36
S15	1	substrate and (gates adj electrically adj coupled adj series) and "257"/\$. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:26
S16	5	substrate and (gates adj coupled adj series) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:27
S17	56	substrate and (gates adj coupled adj together) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:29

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S18	0	substrate and (gates adj coupled adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:29
S19	61	substrate and (gate adj electrodes adj electrically adj coupled) and "257"/\$. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:36
S20	15	substrate and (gate adj electrodes adj coupled adj together) and "257"/\$. ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:39
S21	0	substrate and (gate adj electrodes adj coupled adj one adj another) and "257"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 14:39
S22	324	gate and (sidewall adj spacer) and (ild or (interlayer adj dielectric)) and (doped adj (polysilicon or polycrystalline))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 16:07
S23	2	"6613623".pn. and resistance	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/05 16:07